



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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clm

Application No.: 10/085,432

Filed: February 28, 2002

Inventor(s):

Mark E. Pascual, Michael G.  
Lavelle, Nandini Ramani and  
Patrick Shehane

Title: ARBITRATION SCHEME  
FOR EFFICIENT  
PARALLEL PROCESSING

§ Examiner: Nguyen, Hau H.  
§ Group/Art Unit: 2676  
§ Atty. Dkt. No: 5181-87600  
§ P6070  
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Mark K. Brighwell

Mark K. Brighwell April 29, 2004  
Signature Date

RESPONSE TO OFFICE ACTION OF  
JANUARY 29, 2004

RECEIVED

MAY 06 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Technology Center 2600

Dear Sir:

This paper is submitted in response to the Office Action of January 29, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

05/11/2004 NPETERSO 00000001 501505 10005432

01 FC:1201

86.00 DA

02 FC:1202

18.00 DA